WHAT IS CLAIMED IS:

1. A method for testing a set of interface connections in a reconfigurable device between an IP core implementing at least one specialized operation and a set of functional blocks adapted to implement general-purpose logic devices, the method comprising:

creating a test program including a set of test data and a test configuration adapted to configure the set of functional blocks to implement a set of boundary scan registers connected with the interface connections of the IP core;

configuring the reconfigurable device according to the test configuration;

inputting the test data into the reconfigurable device to create a set of test results; and

analyzing the set of test results to determine the integrity of the set of interface connections.

2. The method of claim 1, wherein the set of boundary scan registers include a plurality of shift registers connected in series, wherein each shift register is adapted to be connected with an interface connection of the IP core.

3. The method of claim 2, wherein a first portion of the plurality of shift registers is adapted to be connected with a set of input interface connections of the IP core and a second portion of the plurality of shift registers is adapted to be connected with a set of output interface connections of the IP core.

4. The method of claim 1, wherein the test configuration is defined with a hardware description language representation.

5. The method of claim 4, wherein the creating a test program includes:

combining the hardware description language representation of the test configuration with a hardware description language representation of the IP core to form a test hardware description; and

analyzing the test hardware description to create a set of test data.

6. The method of claim 5, wherein creating a test program further includes analyzing the test hardware description and the set of test data to create a set of expected test results; and

wherein analyzing the test results includes comparing the set of test results with the set of expected test results.

7. The method of claim 5, wherein analyzing the test hardware description is performed using automated test program generation software.

8. An information storage medium including a set of instructions adapted to operate an information processing device to perform a set of steps, the set of steps comprising:

creating a test program including a set of test data and a test configuration adapted to configure the set of functional blocks to implement a set of boundary scan registers connected with the interface connections of the IP core;

configuring the reconfigurable device according to the test configuration;

inputting the test data into the reconfigurable device to create a set of test results; and

analyzing the set of test results to determine the integrity of the set of interface connections.

9. The information storage medium of claim 8, wherein the set of boundary scan registers include a plurality of shift registers connected in series, wherein each shift register is adapted to be connected with an interface connection of the IP core.

10. The information storage medium of claim 9, wherein a first portion of the plurality of shift registers is adapted to be connected with a set of input interface connections of the IP core and a second portion of the plurality of shift registers is adapted to be connected with a set of output interface connections of the IP core.

11. The information storage medium of claim 8, wherein the test configuration is defined with a hardware description language representation.

12. The information storage medium of claim 11, wherein the creating a test program includes:

combining the hardware description language representation of the test configuration with a hardware description language representation of the IP core to form a test hardware description; and

analyzing the test hardware description to create a set of test data.

13. The information storage medium of claim 12, wherein creating a test program further includes analyzing the test hardware description and the set of test data to create a set of expected test results; and

wherein analyzing the test results includes comparing the set of test results with the set of expected test results.

14. The information storage medium of claim 12, wherein analyzing the test hardware description is performed using automated test program generation software.

15. An information storage medium including a test configuration for configuring a reconfigurable device, the reconfigurable device having an IP core implementing at least one specialized operation and a set of functional blocks adapted to implement general-purpose logic devices, the test configuration comprising a configuration of the set of functional blocks implementing a set of boundary scan registers connected with a set of interface connections of the IP core.

16. The information storage medium of claim 15, wherein the set of boundary scan registers include a plurality of shift registers connected in series, wherein each shift register is adapted to be connected with an interface connection of the IP core.

17. The information storage medium of claim 16, wherein a first portion of the plurality of shift registers is adapted to be connected with a set of input interface connections of the IP core and a second portion of the plurality of shift registers is adapted to be connected with a set of output interface connections of the IP core.

18. The information storage medium of claim 15, wherein the test configuration is defined with a hardware description language representation.

19. The information storage medium of claim 15, further including a set of test data adapted to be input into the IP core via the set of functional blocks implementing the set of boundary scan registers.

20. The information storage medium of claim 15, further including a set of expected test results.